



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ji-Young KIM, et al.

Serial No. 10/759,239

Filed: January 20, 2004

For: VERTICAL DOUBLE-CHANNEL SILICON-ON-
INSULATOR TRANSISTOR AND METHOD OF
MANUFACTURING THE SAME

Notice of Allowance

Mailed: June 17, 2005

Art Unit: 2814

Examiner: Long PHAM

Confirmation No. 8062

INQUIRY CONCERNING FORM PTO-892
ISSUED WITH NOTICE OF ALLOWANCE/NOTICE OF ALLOWABILITY

Mail Stop ISSUE FEE

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:


The Notice of Allowability issued June 17, 2005, indicated [on page 1, under "Attachment(s)", paragraph "1"] that a Notice of References Cited (PTO-892) formed a part of the Action. This form, properly captioned for this application, was received with the Action. However, no references were listed on the form.

Applicant is therefore submitting this inquiry as to whether the Examiner intended to cite references in addition to those cited by applicant and listed on Form PTO-1449 (*modified*), an initialed copy of which also formed a part of the Notice of Allowability.

Respectfully submitted,

August 2, 2005

Date


Eugene M. Lee
Registration No. 32,039

LEE & MORSE, P.C..
1101 WILSON BOULEVARD, SUITE 2000
ARLINGTON, VA 22209
703.525.0978 TEL
703.525.4265 FAX